Amendments to the Claims:

The following listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) A data transfer control device for transferring data among a plurality of nodes that are connected to a bus, the data transfer control device comprising:

a control circuit which starts transfer processing when a processing unit issues a start command for data transfer, and resumes transfer processing when the processing unit issues a resume command for data transfer;

a transfer execution circuit which executes processing for dividing transfer data into a series of packets and transferring the divided series of packets continuously, when the processing unit issues the start command for data transfer;

a cancellation circuit which cancels an execution of one of the start command and the resume command, when the processing unit issues one of the start command and the resume command, respectively, during a period of a reset that clears node topology information; and

a circuit which informs the processing unit that command execution has been canceled by the ~~reset.~~reset,

wherein the cancellation circuit receives a reset period signal which becomes active during the period of the reset and makes a command signal corresponding to one of the issued start command and the issued resume command not to be transferred to the control circuit when the reset period signal becomes active, and

wherein the control circuit performs one of transfer starting and transfer resuming when the processing unit issues one of the start command and the resume command during a period other than the period of the reset.

2. (Previously Presented) The data transfer control device as defined in claim 1, further comprising:

an interrupt controller which issues an interrupt with respect to the processing unit when an execution of the start command or the resume command for data transfer is cancelled by the occurrence of the reset; and

factor storage register which informs the processing unit of a factor of the interrupt.

3. (Previously Presented) The data transfer control device as defined in claim 1,

wherein the cancellation circuit cancels the start command or the resume command by using a signal that goes active during the reset period to mask a signal that goes active when the processing unit issues the start command or the resume command.

4. (Previously Presented) The data transfer control device as defined in claim 1, further comprising:

a pause control circuit which pauses transfer processing at a previously determined location when the processing unit issues a data transfer pause command or when a transfer error occurs.

5. (Canceled)

6. (Original) The data transfer control device as defined in claim 1,

wherein the reset is a bus reset as defined by the IEEE 1394 standard.

7. (Currently Amended) A data transfer control device for transferring data among a plurality of nodes that are connected to a bus, the data transfer control device comprising:

a transfer execution circuit which executes processing for dividing transfer data into a series of packets and transferring the divided series of packets continuously, when processing unit issues a start command for data transfer; and

a pause control circuit which pauses a transfer processing after a step execution of the transfer processing, when the processing unit issues a resume command and a pause command for data transfer ~~together.~~together,

wherein the pause control circuit pauses the transfer processing after the completion of the transfer processing of the step execution.

8. (Previously Presented) The data transfer control device as defined in claim 7,

wherein the pause control circuit executes the step execution and the pause of the transfer processing, based on a resume signal that goes active when the resume command is issued, and a delay pause signal that goes active after a delay of a given period after the resume signal goes active when the resume command and the pause command are issued together.

9. (Original) The data transfer control device as defined in claim 1,

wherein data transfer is performed in accordance with the IEEE 1394 standard.

10. (Original) The data transfer control device as defined in claim 7,

wherein data transfer is performed in accordance with the IEEE 1394 standard.

11. (Original) Electronic equipment comprising:

the data transfer control device as defined in claim 1;

a device for performing given processing on data that has been received from another node via the data transfer control device and the bus; and

a device for outputting or storing data that has been subjected to the processing.

12. (Original) Electronic equipment comprising:

the data transfer control device as defined in claim 6;

a device for performing given processing on data that has been received from another node via the data transfer control device and the bus; and

a device for outputting or storing data that has been subjected to the processing.

13. (Original) Electronic equipment comprising:

the data transfer control device as defined in claim 7;

a device for performing given processing on data that has been received from another node via the data transfer control device and the bus; and

a device for outputting or storing data that has been subjected to the processing.

14. (Original) Electronic equipment comprising:

the data transfer control device as defined in claim 9;

a device for performing given processing on data that has been received from another node via the data transfer control device and the bus; and

a device for outputting or storing data that has been subjected to the processing.

15. (Original) Electronic equipment comprising:

the data transfer control device as defined in claim 10;

a device for performing given processing on data that has been received from another node via the data transfer control device and the bus; and

a device for outputting or storing data that has been subjected to the processing.

16. (Original) Electronic equipment comprising:

the data transfer control device as defined in claim 1;

a device for performing given processing on data that is to be transferred to another node via the data transfer control device and the bus; and

a device for fetching data to be subjected to the processing.

17. (Original) Electronic equipment comprising:

the data transfer control device as defined in claim 6;

a device for performing given processing on data that is to be transferred to another node via the data transfer control device and the bus; and

a device for fetching data to be subjected to the processing.

18. (Original) Electronic equipment comprising:

the data transfer control device as defined in claim 7;

a device for performing given processing on data that is to be transferred to another node via the data transfer control device and the bus; and

a device for fetching data to be subjected to the processing.

19. (Original) Electronic equipment comprising:

the data transfer control device as defined in claim 9;

a device for performing given processing on data that is to be transferred to another node via the data transfer control device and the bus; and

a device for fetching data to be subjected to the processing.

20. (Original) Electronic equipment comprising:

the data transfer control device as defined in claim 10;

a device for performing given processing on data that is to be transferred to another node via the data transfer control device and the bus; and

a device for fetching data to be subjected to the processing.

21. (Canceled)